

EAST Search History**EAST Search History (Prior Art)**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S136	30971	(processor cpu) with (gpu graphic\$1 adj2 (accelerator controller card processor))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/11/10 17:24
S137	49243	(adjust\$4 increas\$3 decreas\$3 rais\$3 lower\$3 set\$4 chang\$3 modif\$7) near4 (performance speed frequency clock) with (processor cpu)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/11/10 17:25
S139	25	S136 and S138	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/11/10 17:26
S138	188	S137 same (frame adj rate frame-rate framerate deadline dead-line dead adj line (refresh adj2 rate))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/11/10 17:26
S140	9	S139 and @ad< "20040330"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/11/10 17:27
S141	30633	S136 and graphic\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/11/10 17:35
S144	163	S137 same (frame adj rate frame-rate framerate deadline dead-line dead adj line)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/11/10 17:36

S143	25	S142 and @ad< "20040330"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/11/10 17:36
S142	60	S138 and graphic\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/11/10 17:36
S146	17	S145 and @ad< "20040330"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/11/10 17:37
S145	43	S144 and graphic\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/11/10 17:37
L1	10	deadline adj margin	US-PGPUB; USPAT; EPO	OR	OFF	2009/11/18 16:39
L2	1	1.clm.	US-PGPUB; USPAT; EPO	OR	OFF	2009/11/18 16:40

EAST Search History (Interference)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L5	1	4.clm.	US-PGPUB; USPAT; UPAD	OR	OFF	2009/11/18 16:40
L4	10	deadline adj margin	US-PGPUB; USPAT; UPAD	OR	OFF	2009/11/18 16:40
L3	106755	713/3\$2.ccls. "345"/\$. ccls.	US-PGPUB; USPAT; UPAD	OR	OFF	2009/11/18 16:40
L7	1	4 and 6	US-PGPUB; USPAT; UPAD	OR	OFF	2009/11/18 16:41
L6	380	(first second) adj processor with (graphic\$1 render\$3)	US-PGPUB; USPAT; UPAD	OR	OFF	2009/11/18 16:41
L11	2431	adjust\$4 near6 (operating adj point)	US-PGPUB; USPAT; UPAD	OR	OFF	2009/11/18 16:42
L10	0	clock adj speed with repeatedly adj set	US-PGPUB; USPAT; UPAD	OR	OFF	2009/11/18 16:42
L9	1	8.clm.	US-PGPUB; USPAT; UPAD	OR	OFF	2009/11/18 16:42

L8	2	first adj processor with interrupt with graphics	US-PGPUB; USPAT; UPAD	OR	OFF	2009/11/18 16:42
L14	1	13.clm.	US-PGPUB; USPAT; UPAD	OR	OFF	2009/11/18 16:43
L13	3	11 with (first second) adj (task processor)	US-PGPUB; USPAT; UPAD	OR	OFF	2009/11/18 16:43
L12	1	11 with (first second) adj (task processor)	US-PGPUB; USPAT; UPAD	OR	OFF	2009/11/18 16:43

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